

Figure 9

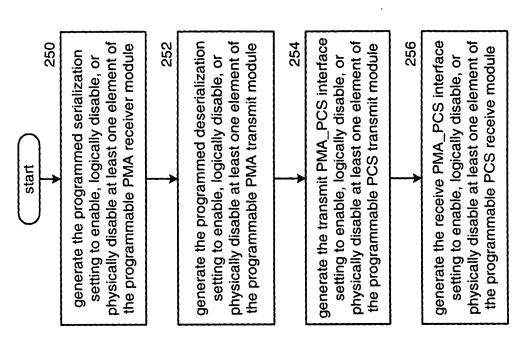


Figure 10

0x00 MASTERBIAS 0x01 TXDIVRATIOLO 0x02 TXDIVRATIOHI 0x03 TXLOOPFILTER 0x04 TXMODECONTROL 0x05 TXOUTPUTLEVEL 0x06 TXOUTPUTLEVEL 0x07 RXDIVRATIOLO 0x08 RXLOOPFILTER 0x09 RXLOOPFILTER 0x09 RXMODE1 0x08 RXMODE1 0x08 RXMODE1 0x00 RXFEICONTROL1 0x00 RXFEICONTROL1 0x00 RXFEICONTROL1 0x00 RXFEICONTROL1 0x00 RXFEICONTROL1	9	4	Remains a control of the control of	0
TXDIVRATIOLIO TXDIVRATIOHI TXLOOPFILTER IBOOST TXMODECONTROL TXOUTPUTLEVEL TXOUTPUTLEN RXDIVRATIOLO RXDIVRATIOLO RXDIVRATIOLO RXECICONTROL ATERICONTROL ATERICONTROL TXDRVEN POWERCONTROL TXDRVEN TXDRVEN TXDRVEN	VCODAC[5:0]	[0]		MASTERBIAS[1:0]
TXDIVRATIOHI TXLOOPFILTER IBOOST TXMODECONTROL TXOUTPUTLEVEL T		TXDIVRATIO[7:0]	10[7:0]	
TXLOOPFILTER IBOOST TXMODECONTROL TXOUTPUTLEVEL	Emperature of the Control of the Con		TXBUSWID	TXDIVRATIO[9:8]
TXMODECONTROL TXOUTPUTLEVEL TXOUTPUTMODE RXDIVRATIOLO RXDIVRATIOHI RXLOOPFILTER RXMODE0 RXMODE1 RXMODE1 RXFEICONTROL1 <reserved> TXDRVEN POWERCONTROL1 TXDRVEN RXMODE1 TXDRVEN TXDRVEN RXFEICONTROL1 TXDRVEN</reserved>	18		TXLOOPFILTERR[1:0]	TXLOOPFILTERC[1:0]
TXOUTPUTLEVEL TXOUTPUTMODE RXDIVRATIOLO RXDIVRATIOHI RXLOOPFILTER RXMODE0 RXREC RXMODE1 RXFEICONTROL1 <reserved> TXDRVEN POWERCONTROL1 TXDRVEN</reserved>	•	TXVSEL[1:0]	TXVCOGAIN TXVCODAC	TXCPI
TXOUTPUTMODE RXDIVRATIOLO RXLOOPFILTER RXMODE0 RXMODE1 RXFEICONTROL0 RXFEICONTROL1 <reserved> TXDRVEN POWERCONTROL1 TXDRVEN</reserved>	SLEW EMPOFF	PRDRVOFF	TXDOWNLEVEL[3:0]	EVEL[3:0]
RXDIVRATIOLO RXLOOPFILTER RXMODE0 RXMODE1 RXMODE1 RXFEICONTROL0 RXFEICONTROL1 <pre>creserved></pre> TXDRVEN	TXANASW	/ TXDIGSW	TXEMPHLEVEL(3:0)	EVEL[3:0]
RXLOOPFILTER RXMODE0 RXMODE1 RXMODE1 RXFEICONTROL0 RXFEICONTROL1 <pre>creserved></pre> CTORNORN TXDRVEN		RXDIVRATIO[7:0]	10[7:0]	
RXMODE0 RXMEG RXMODE1 RXFEICONTROL0 RXFEX Areserved> CONFECCINTROL1 ARECONTROL1 ARECONTROL		e dies de Angel e Haddiges (Aldige e y New Mark to a diese e montre	RXDIVRATIO[13:8]	
RXMODE1 RXMODE1 RXFEICONTROL0 RXFEICONTROL1 <reserved> TXDRVEN RXMERCONTROL TXDRVEN</reserved>		BXLC	RXLOOPFILTERR[2:0]	RXLOOPFILTERC[1:0]
RXFEICONTROL0 RXFER RXFEICONTROL1 <reserved></reserved>		RXVSEL[1:0]	RXVCOGAIN RXVCODAC	RXCPI RXVCOSW
RXFEICONTROL0 RXFEICONTROL1 <reserved> Creserved> RXFEICONTROL1 TXDRVEN</reserved>	RXCPGAIN RXVS	RXVSELCP[1:0]		
RXFEICONTROL1	AXFER[1:0]		RXFEI[1:0]	VSELAFE[1:0]
<pre><reserved></reserved></pre>		RXFER[9:2]	[9:2]	
POWERCONTRO! TXDRVEN				A PARA PARA PARA PARA PARA PARA PARA PA
	EN RXEN TXEN		RXANAEN TXDIGEN	TXANAEN BIASEN
0x10-0x3F <reserved></reserved>				

Figure 11
PMA memory mapped register 45

Primitive	Standard	Serial Rate	Encoding	Fabric Interface
GT10_CUSTOM	•	≤10.3125 Gbps	Any	q08-q8
GT10_10GFC_8	10G Fibre Channel 10.51875 Gbps	10.51875 Gbps	64b/66b	64b@159.37MHz
GT10_10GFC_4	10G Fibre Channel 10.51875 Gbps	10.51875 Gbps	64b/66b	32b@318.75MHz
GT10_AURORAX_8	AuroraX	≤10.3125 Gbps	64b/66b	64b@156.25MHz
GT10_AURORAX_4	AuroraX	≤10.3125 Gbps	64b/66b	32b@312.5MHz
GT10_10GE_8	10GE 10GBase-R	10.3125 Gbps	64b/66b	64b@156.25MHz
GT10_10GE_4	10GE 10GBase-R	10.3125 Gbps	64b/66b	32b@312.5MHz
GT10_OC192_8	SONET OC-192	9.95328 Gbps	None	64b@155.52MHz
GT10_OC192_4	SONET OC-192	9.95328 Gbps	None	32b@311.02MHz

Figure 12A

Primitive	Standard	Serial Rate	Encoding	Fabric Interface
GT10_FCXAUI_4	10GFC (XAUI)	3.1875 Gbps	8b/10b	32b@79.6875MHz
GT10_FCXAUI_2	10GFC (XAUI)	3.1875 Gbps	8b/10b	16b@159.375MHz
GT10_FCXAUL_1	10GFC (XAUI)	3.1875 Gbps	8b/10b	8b@318.75MHz
GT10_XAUI_4	10GE (XAUI)	3.125 Gbps	8b/10b	32b@78.125MHz
GT10_XAUI_2	10GE (XAUI)	3.125 Gbps	8b/10b	16b@156.25MHz
GT10_XAUI_1	10GE (XAUI)	3.125 Gbps	8b/10b	8b@312.5MHz
GT10_AURORA_4	Aurora	3.125 Gbps	8b/10b	32b@78.125MHz
GT10_AURORA_2	Aurora	3.125 Gbps	8b/10b	16b@156.25MHz
GT10_AURORA_1	Aurora	3.125 Gbps	8b/10b	8b@312.5MHz

Figure 12B

Primitive	Standard	Serial Rate	Encoding	Fabric Interface
GT10_INFINIBAND_4	InfiniBand	2.5 Gbps	8b/10b	32b@62.5MHz
GT10_INIFINIBAND_2	InfiniBand	2.5 Gbps	8b/10b	16b@125MHz
GT10_INIFINIBAND_1	InfiniBand	2.5 Gbps	8b/10b	8b@250MHz
GT10_3GIO_4	PCI Express	2.5 Gbps	8b/10b	32b@62.5MHz
GT10_3GIO_2	PCI Express	2.5 Gbps	8b/10b	16b@125MHz
GT10_3GIO_1	PCI Express	2.5 Gbps	8b/10b	8b@250MHz
GT10_OC48_4	SONET OC-48	2.488 Gbps	None	32b@77.76MHz
GT10_OC48_2	SONET OC-48	2.488 Gbps	None	16b@155.52MHz
GT10_OC48_1	SONET OC-48	2.488 Gbps	None	8b@311.04MHz

Figure 12C